## **Advertisement for the Post of Lab Engineer (On Contract)**

Applications are invited for the post of Lab Engineer (on contract) in DeitY sponsored project titled "Special Manpower Development programme for Chips to System design (SMDP-C2SD)". The duly completed application on prescribed format along with copies of supporting documents must reach to Principal Investigator: Dr. Manish Goswami, Assistant Professor, Department of Electronics & Communication Engineering, Room No.2106, Computer Centre–1, Indian Institute of Information Technology Allahabad, Devghat, Jhalwa, Allahabad, Uttar Pradesh 211012 on or before 30/09/2016. The envelope should be super scribed as "Application for the post of Lab Engineer on contract in the Research Project "Special Manpower Development programme for Chips to System design (SMDP-C2SD)". A soft copy of the application should also be sent to hod.ec@iiita.ac.in. The position is purely temporary and will be governed by the Funding agency rules & service conditions of IIIT Allahabad.

Name of Position: Lab Engineer

Number of Position: 01 (one)

Focus Area: Zigbee Transreceiver design and fabrication

**Project Duration**: Appointment will be made on contract basis for a period of one year initially, which may be extended depending on the performance evaluation on yearly basis till the end of the project.

**Essential Qualifications**: B.Tech. (ECE) and M.Tech. (VLSI/Embedded System/Microelectronics or equivalent) with 6.5 CGPA (or 60% marks) at B.Tech. & M.Tech. level and having relevant experience in VLSI related projects.

**Desirable Qualifications**: Knowledge of VLSI design Tools

Consolidated Salary: 30000/-pm (Fixed).

## Note:

- 1. The applicant will be responsible for the authenticity of information, other documents and photographs submitted.
- 2. The Institute reserves the right to accept application at any time, and consider candidates of exceptional credentials without applications. Qualification and experience may be relaxed by the Institute at any point of time for otherwise exceptional candidates.
- 3. Mere, possessing the prescribed qualification does not ensure that the candidate would be called for Interview. The Candidates will be shortlisted on the basis of merit and need of the project.
- 4. Shortlisted Candidates will be informed by email about the interview date. So, the Candidate must provide valid E-mail IDs in their applications.
- 5. Shortlisted candidates have to present themselves for the interview on the interview date with updated CV and original and attested photocopies of mark sheets/ certificates in support of their academic qualifications.
- 6. Applicants in employment (private, government or any other organization) are required to submit a "No Objection Certificate" from the employer at the time of interview.
- 7. No TA/DA will be paid for appearing in the interview.

Name of Principal Investigator: Dr. Manish Goswami

**Designation**: Assistant Professor

**Department of Electronics and Communication Engineering** 

## INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, ALLAHABAD



(A University Established under Sec.3 of UGC Act, 1956 vide Notification No. F.9-4/99-U.3 Dated 04.08.2000 of the Govt. of India)

(A Centre of Excellence in Information Technology)

Deoghat Jhalwa, Allahabad-211012 (U.P.), India

Ph: 0532-2922101, Fax:0532-2922091, Web: www.iiita.ac.in, E-mail: hod.ec@iiita.ac.in

## **Application Format**

<b>Application for Post of</b>	f <u>Lab Engineer (</u> on	contract) in	research pro	ject entitled	"Special Manpower
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Place:..... Signature of Applicant